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## P.A.B.X. EQUIPMENT APPROVAL TESTING

COMMONWEALTH OF AUSTRALIA

## POSTMASTER-GENERAL'S DEPARTMENT

HEADQUARTERS

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## PABX EQUIPMENT APPROVAL TESTING SECTION 1. INTRODUCTION AND GENERAL

(i) This Specification standardises approval test conditions for the more important network interface standards stipulated in APO Specifications 1080 and 1105. Some other miscellaneous requirements of these Specifications are also covered. Reference should be made to Specifications 1080 and 1105 for the design objectives required.
(ii) The tests covered in this Specification are not for use during acceptance testing of complete installations in field situations; they are for use during examination of systems for compliance with design objectives.
(iii) Test circuits have been presented in 3 groups.
(a) Transmission Tests.
(b) Decadic Pulsing Tests.
(c) Other signalling Tests.

### 1.2 General Requirements

(i) The ambient temperature for all tests shall be recorded in degrees Celcius.
(ii) The voltage applied to equipment shall be the nominal voltage for the system (50V) unless otherwise specified. Where tests specify minimum and maximum voltages, they shall be the manufacturers minimum and maximum design voltages.
(iii) The accuracy of meters and components in the test arrangements shall be better than $1 \%$.
(iv) The test circuits shall be protected from external interference using shields, earth connection, etc., where appropriate.
(v) The adjustments to components in the items under tests shall be in accordance with the manufacturer's design specification.
(vi) The mounting of items under test shall be equivalent to their normal operating position in working equipment.
(vii) Where two or more items may affect the results of a test, they shall be associated and tested simultaneously. Results shall not be synthesised from independent readings.
(viii) Where the results of a test are marginal, i.e. close to the acceptable limit, a second and third sample item of the same type shall be tested and all of the results recorded.
(ix) Where results are identical for tests on different classifications of exchange line circuits, one set of results may be recorded on the CE-99033 sheets provided that all different classifications are indicated on the same sheet.
(x) Where the following sections specify the testing of exchange line circuits, tie line circuits, shall also be tested accordingly.

## SECTION 2. TRANSMISSION TESTS

2.1 Reference Equivalent of PABX Transmission Circuits
(i) Reference equivalents shall be measured either
(a) using the Bruel and Kjaer Electroacoustic Transmission Measuring Equipment, in accordance with "Bruel and Kjaer, Instructions and Applications Type 3350/51". or
(b) any other approved method.
(ii) Transmit, Receive and Sidetone reference equivalents of the operator's transmission circuit, shall be measured for line lengths $0,0.5,1,2$, and 3 miles of $4 \mathrm{Ib} / \mathrm{mile}$ cables and graphed on CE-99033 Sheet 1.
(iii) The transmit Receive, Sidetone and Overall reference equivalent for an extension to extension connection shall be measured at a line length of 0.1 mile.
(iv) Results shall be recorded on CE-99033 Sheet 1 in $d B$.

### 2.2 Insertion Loss

(i) Insertion loss shall be measured for all types of exchange lines circuits. Test conditions shall be in accordance with Fig. 1.
(ii) Circuit conditions:-
(a) Speaking normal - extension to public exchange
(b) Speaking operator - operator only to public exchange, the operator's circuit replaced by the test circuit.
(c) Monitoring operator - the insertion loss between the extension line terminals and the exchange line terminals shall be measured whilst the operator's circuit is monitoring the call.
(d) Any other condition which may interpose the exchange line circuit between the public exchange and an extension line or operator shall be measured.
(iii) The accuracy of the readings is to be $\pm 0.05 \mathrm{~dB}$ in 0.1 dB steps.
(iv) The results shall be tabulated on CE-99033 Sheet 2 .
2.3 Impedance Regularity
(i) Return loss shall be measured for all types of exchange line circuits. Test conditions shall be in accordance with Fig. 2.
(ii) Circuit conditions:-
(a) Speaking normal - extension to public exchange.
(b) Speaking operator - operator only to the public exchange, the operator's circuits to be complete but the transmitter and receiver may be mechanically muted to prevent interference to the measurements. A suitable resistor may be used to replace the operator's transmitter.
(c) Monitoring operator - extension to public exchange with the operator's circuit monitoring (listening) to the call.
(d) Enquiry - whilst holding a public exchange call if it is possible for the extension to initiate another call to the operator or another extension or another exchange line, the terminating condition of the public exchange call shall be measured.
(e) Transfer - if it is possible to pass an established public exchange call from one transmission termination to another, i.e. between extensions, operators or hold circuits in any combination, a measurement shall be taken during the transfer.
(f) Holding - switchboard to public exchange with the operator's hold circuit operated.
(g) Any other exchange line terminating condition that may occur between the end of signalling of a public exchange call and clearing shall be measured.
(iii) The accuracy of readings shall be $\pm 0.25 \mathrm{~dB}$ in 0.5 dB steps.
(iv) The results shall be tabulated on CE-99033 Sheet 3.

### 2.4 Balance to Earth

(i) Test condition in accordance with Fig. 3.

Voltage measurements shall be taken across both sides of the transmission circuit, each being terminated in 600 ohm non-reactive impedances. Obtain voltage readings (EM) at BB and $\mathrm{B}_{1} \mathrm{BI}_{1}$ whilst an e.m.f. (EI) of 1.55 volts RMS and negligible internal impedance is applied between earth and the mid point of, first one then the other 600 ohm terminating impedance. The line dc on each side shall be varied within the range $10-100 \mathrm{~mA}$ and the frequency of El varied from 30 Hz to 50 KHz .
(ii) Circuit condition - All possible connecting circuits where transmission between extension and public exchange, and extension to extension is possible.
(iii) The accuracy of readings shall be $\pm 0.5$ millivolts in 1 millivolt steps.
(iv) The smallest value of Impedance Balance ratio ( $20 \log \frac{E l}{E M}$ ) in each of the frequency ranges, 30 Hz to $600 \mathrm{~Hz}, 600 \mathrm{~Hz}$ to $3,400 \mathrm{~Hz}$ and $3,400 \mathrm{~Hz}$ to 50 kHz , shall be tabulated on CE-99033 Sheet 4.
2.5 Overlap Circuit
(i) Test conditions in accordance with Fig. 4. Outgoing, Bothway and Incoming exchange line circuits shall be tested.
(ii) Circuit condition - operator in overlap condition on one exchange line whilst attending an incoming or revertive outgoing call on another exchange line.
(iii) The accuracy of readings shall be
$\pm 0.05 \mathrm{~dB}$ in 0.1 dB steps for Insertion Loss measurements
$\pm 0.25 \mathrm{~dB}$ in 0.5 dB steps for Return
$\pm 0.25 \mathrm{~dB}$ in 0.5 dB steps for Return loss measurements
(iv) The results shall be tabulated on CE-99033 Sheet 5.

### 2.6 Active and Non-linear Circuit Performance

Any circuit which contains amplifying or non-linear components shall be tested for harmonic distortion and intermodulation distortion as follows:-
(i) Test condition - in accordance with Fig. 5.
(ii) Circuit condition - normal operating conditions.
(iii) The accuracy of results shall be $\pm 0.5 \mathrm{~dB}$ in 1 dB steps.
(iv) Results shall be recorded on CE-99033 Sheet 15.


FIG. 1. PABX APPROVAL TESTS
INSERTION LOSS.


FIG. 2. PABX APPROVAL TESTS




EXAMPLE FOR NORMAL NSTRUMENTS CALIBRATED ACROSS $600 \Omega$
$=10 L O G \frac{\mathrm{R}}{\mathrm{R} 2}=10 L O G \frac{600}{150}=10$ LOG $4=10 \times 0.6021 \simeq 6 \mathrm{~dB}$.



FIG. 5. PABX APPROVAL TESTS
ACTIVE \& NON-LINEAR CIRCUIT PERFORMANCE.

## SECTION 3. DECADIC PULSING TESTS

### 3.1 General.

The tests in the section shall apply to operator, extension and/or keysender dialling for all bothway and outgoing exchange line circuits.

### 3.2 Pulse Source Resistance.

(i) Test condition in accordance with Fig. 6 Pulse Source Resistance.
(ii) Circuit condition equivalent to the remake after a normal break pulse.
(iii) The accuracy of readings shall be $\pm 1$ ohm in 2 ohm steps.
(iv) The results shall be entered on CE-99033 Sheet 6 .
3.3 Peak Voltage on Line During Pulsing.
(i) Test condition in accordance with Fig. 6 Peak Voltage and Waveshape on Line During Pulsing. The cathode ray oscilloscope must have a high impedance balanced differential inputs. An earthed input cathode ray oscilloscope must not be used.
(ii) Circuit condition - continuous pulses applied to the extension side of the repeating bridge of the exchange line circuit.
(iii) The readings shall be the algebraic sum of the maximum peak voltage negative from zero and the maximum peak voltage positive from zero during any train of pulses.
(iv) The accuracy of the readings shall be $\pm 5$ volts in 10 volt steps.
(v) The results shall be tabulated on CE-99033 Sheet 6 .
3.4 Wave Shape on Line During Pulsing.
(i) Test condition in accordance with Fig. 6 Peak Voltage and Waveshape on Line During Pulsing. The cathode ray oscilloscope must have high impedance balanced differential inputs. An earthed"input cathode ray oscilloscope must not be used. A high quality camera attachment is required.
(ii) Circuit condition-one train of pulses applied to the extension side of the repeating bridge of the exchange line circuit with the exchange line in the "A" condition.
(iii) One photographic print shall show the lst break pulse wave shape and another shall show the 2nd break pulse wave shape against a grid scale background showing voltage and time. The trace lines shall be sharp and clear.
(iv) The photographic prints shall be approximately $8.5 \mathrm{~cm} \times 10.5 \mathrm{~cm}$ each and shall be glued to CE-99033 Sheet 6.
3.5 Keysender Performance.

Keysenders shall be tested in accordance with sections 3.2, 3.3, 3.4 and also to the requirements of Fig. 7. Results shall be tabulated on CE-99033 sheets 6 and 7, and marked "keysender Performance".

### 3.6 Pulse Performance Using Digital Display Timer.

(i) Tests condition in accordance with Fig. 8. The pulse gate circuit shall be to CSKl2219, or equivalent, employing the polarised relay as the pulse receiver.
(ii) The circuit condition of the exchange line circuit shall be equivalent to an extension telephone dialling out to the public exchange.
(iii) Separate tests shall be conducted with 50,52 and the maximum and minimum volts applied to the PABX equipment.
(iv) The optimum target points (speed 10 pps , make ratio $34 \%$ ) shall be tested first to check the repeating relay adjustments.
(v) The accuracy of the readings shall be $\pm 0.5$ milliseconds in 1 millisecond steps.
(vi) For each measurement, five consecutive readings shall be taken and averaged to the nearest 0.5 millisecond except that, if there is a spread of 3 milliseconds or more between the shortest and longest readings a fault shall be said to exist.
(vii) The average of five consecutive readings rounded off to the nearest 0.5 millisecond shall be tabulated in the appropriate co-ordinate on CE-99033 Sheet 8.
(viii) For each setting of the adjustable pulse generator, its output should be fed to the pulse gate selector and timer and measured.
(ix) For each target point at each voltage the measurements should be made in the following order:-
(a) pulse generator output lst make pulse.
(b) line condition "A" lst make pulse,
(c) line condition " $B$ " lst make puise.
(d) line condition "C" lst make pulse.
(e) pulse generator output 5 th make pulse.
(f) Iine condition "A" 5th make pulse.
(g) line condition " $B$ " 5 th make pulse.
(h) line condition "C" 5th make pulse.
(i) pulse generator output 9 th make pulse.
(j) line condition "A" 9th make pulse.
(k) line condition " $B$ " 9th make pulse.
(1) line condition "C" 9th make pulse.
(m) pulse generator output lst break pulse.
( n ) line condition "A" lst break pulse.
(o) Iine condition " $B$ " Ist break pulse.
(p) line condition "C" lst break pulse.
(q) pulse generator output 2nd break pulse.
(r) line condition "A" 2nd break pulse.
(s) line condition " $B$ " 2nd break pulse.
( $t$ ) line condition " C " 2nd break pulse.
(u) pulse generator output 6 th break pulse.
(v) line condition "A" 6th break pulse.
(w) line condition "B" 6 th break pulse.
(x) line condition "C" 6th break pulse.
(y) pulse generator output loth break pulse.
(z) line condition "A" 10th break pulse.
$\left(z_{1}\right)$ line condition "B" loth break pulse.
( $z_{2}$ ) line condition "C" loth break pulse.
(x) The aggregate make distortion for the lst, 5 th and 9 th make pulses under each line condition is obtained by subtracting the appropriate pulse generator output make pulse tabulation. Thus, if the 5 th make pulse is 34 milliseconds from the generator and 32 milliseconds from the exchange line circuit under line condition A, the aggregate make distortion for line condition $A$ th make pulse is -2 .
(xi) The aggregate break pulse distortion for the lst, 2nd, 6th and loth break pulses under each line condition is obtained by subtracting the appropriate pulse generator output break pulse tabulation from the appropriate line condition break pulse tabulation. Thus if the loth break pulse is 66 milliseconds from the generator and 68 milliseconds from the exchange line circuit under condition $C$, the aggregate break distortion for line condition $C$ loth break pulse is +2 .
(xii) A table of acceptable aggregate distortion of make and break pulses is shown on CE-99033 Sheet 8. Provided that all other pulses are within acceptable limits, lst Breaks that are distorted by no more than +2 at the optimum point, +4 at design points 1,2 and 3 , and +12 at design point 4 may be accepted. (Refer also to APO Specification 1080 Section 9).


ACEEPTED／RENECTED DATE
peak voltage and wave shape on line during pulsing

|  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- |
|  |  |  |  |  |


hLIM 8 on


FIG．6．PABX APPROVAL TESTS
MINIMUM INTERDIGITAL PAUSE

| SUCCESSIVE OPERATION OF KEY | 1 | 5 | 9 |
| :---: | :---: | :---: | :---: |
| SO VOLTS. |  |  |  |
| $\ldots$ VOLTS. (MIN.) |  |  |  |
| $\ldots$ VOLTS. (MAX.) |  |  |  |

1. OPERATE KEY NUMBER I IN RAPID SUCCESSION.
2. MEASURE THE DURATION OF THE LAST MAKE PULSE (IN BETWEEN
PULSE TRAINS). THIS DURATION IS THE MINIMUM INTERDIGITAL
3. REPEAT MEASUREMENTS FOR KEYS NUMBERED 5 AND 9 .
4. RECORD RESULTS IN THE ABOVE TABLE TO THE NEAREST

| SINGLE OPERATION OF KEY | 1 | 5 | 9 |
| :---: | :---: | :---: | :---: |
| C_VOLTS (MIN.) |  |  |  |
| SO VOLTS. |  |  |  |
| VOLTS. (MAX.) |  |  |  |
|  |  |  |  |

1. OPERATE KEY NUMBER I, ONCE.
2. MEASURE THE DURATION BETWEE
MEASURE THE DURATION BETWEEN THE END OF THE LAST BREAK
PULSE AND OCCURANCE OF LOCKOUT TEIS OURATION IS THE
MAXIMUM INTERDIGITAL PAUSE FOR THE KEYSENDER.
3. REPEAT MEASUREMENTS FOR KEVS NUMBERED 5 AND 9.
4 RECORD RESULTS IN THE ABOVE TABLE TO THE NEAREST



FIG. 8. PABX APPROVAF TESTS PULSE PERFORMANCE USING DIGITAL DISPLAY TIMER.

## SECTION 4. OTHER SIGNALLING TESTS

### 4.1 Incoming Ring

### 4.1.1 General.

The following tests apply to all incoming and bothway exchange line circuits, which respond to AC ring from the exchange. Tests shall be done at minimum, 50V and maximum power supply voltage, where indicated in Figs 9 and 10.

### 4.1.2 Effect of Exchange Line Polarity Reversal On Ring Detection Circuitry.

(i) Test conditions in accordance with Fig. 9
(ii) Circuit conditions - normal free condition
(iii) The effect of reversing the exchange line polarity shall be recorded on CE-99033 Sheet 9.
4.1.3 Ring Detection Sensitivity.
(i) Test conditions in accordance with Fig. 9. The variable resistor Rl shall be increased to the maximum value that permits satisfactory response of the ring detection circuit to the $A C$ voltage from the test oscillator.
(ii) Circuit condition - normal
(iii) The accuracy of the readings shall be.
(a) $\pm 5$ ohms in 10 ohm steps.
(b) $\pm 0.5$ milliamps in 1 milliamp steps.
(c) $\pm 0.5$ volts in 1 volt steps.
(iv) Line resistance, line current and line voltage shall be measured and results tabulated on CE-99033 Sheet 9.

### 4.1.4 Preguard Delay After Seizure From Exchange.

(i) Test conditions in accordance with Fig. 10. The switch positions $A$ and $B$ similate the early guard signal sent from either a step by step or a crossbar exchange respectively. The AC signal from a crossbar exchange may arrive at any phase angle. The period between the first appearance of early guard voltage at the PABX MDF and the application of guard by the PABX is the Preguard Delay.
(ii) Circuit conditions - normal.
(iii) The accuracy of readings shall be $\mp 0.5$ millisecond in 1 millisecond steps.
(iv) Results shall be recorded on CE-99033 Sheet 9.
(a) Preguard delay when the ring detection relay is operated manually.
(b) Preguard delay when the switch is in the A position.
(c) The shortest Preguard Delay of a series of at least ten tests, when the switch is in the $B$ position.
(a) The Iongest Preguard Delay of a series of at least ten tests, when the switch is in the $B$ position.
4.1.5 Ring Trip Resistance
(i) Test condition in accordance with Fig. 10
(ii) Circuit condition of the exchange line shall be equivalent to
(a) operator answering an incoming public exchange call
(b) extension answering an incoming nightswitched call.
(iii) The accuracy of readings shall be $\pm 1$ ohm in 2 ohm steps.
(iv) The ring trip resistance shall be measured and results recorded on CE-99033 Sheet 9 .

### 4.1.6 Duration of Ring Trip Conditions.

(i) Test conditions in accordance with Fig. 10
(ii) Circuit conditions - as in 4.1 .5 (ii)
(iii) The accuracy of readings shall be $\pm 5$ milliseconds in 10 millisecond steps.
(iv) The duration of ring trip shall be measured and results tabulated on CE-99033 Sheet 9.
4.2 INDIAL PERFORMANCE.
4.2.1 General.

All indial exchange line circuits and indial tie line circuits shall be tested.
4.2.2 Pick-up Sensitivity on Seizure by Exchange.
(i) Test condition in accordance with Fig. 11, Pick-up Sensitivity.
(ii) Circuit condition - normal. The indial exchange line pick-up circuit shall consist of a balanced circuit feeding 50 volt battery and earth to the exchange line.
(iii) The accuracy of the readings shall be:-
(a) $\pm 5$ ohms in 10 ohm steps.
(b) $\pm 0.5 \mathrm{~mA}$ in 1 milliamp steps.
(iv) The results shall be tabulated on CE-99033 Sheet 10.

### 4.2.3 Impulse Response of Adaress Signals.

(i) Test condition in accordance with Fig. 11, Impulse Response.
(ii) The circuit condition of the indial circuit shall be equivalent to the receiving of loop interrupted dial signalling from the public exchange. Before and after each test train of digits the circuit shall be looped via the appropriate test line condition to allow for normal functioning of the exchange line circuit.
(iii) The received dial pulse store shall have a visual display connected to the output marking circuits so as to accurately display the store count for each train of pulse received. For each test the result shown on this display shall be tabulated on CE-99033, Sheet 10.
(iv) For each setting of the adjustable pulse generator, its output shall be fed to speed and ratio meters and checked.
(v) Separate tests shall be conducted with the maximum volts, 50 volts and the minimum volts applied to the PABX equipment.
(vi) For each specified test pulse condition and at each voltage and for each line condition, trains of 3,6 and 9 pulses, equivalent to the maximum number of digits that may be stored in the equipment, shall be applied. The interdigital pause between each train in a test shall be 1 second.
(vii) Any display which does not correspond to the pulses applied shall be a fault. However, if the incidence of the fault is dependant upon the setting of the pulse generator or train gate or line condition or equipment voltage, the tests shall be completed to show the full extent of the fault.

### 4.2.4 Maximum Pre-dialling Delay Response.

(i) Test condition in accordance with Fig. 12 Maximum Pre-dialling Delay Response.
(ii) The circuit condition of the indial circuit shall be equivalent to receiving a pick up loop signal from the public exchange.
(iii) The accuracy of the readings shall be $\pm 50$ milliseconds in 100 millisecond steps.
(iv) Separate tests shall be conducted with the maximum, 50 and the minimum voits applied to the PABX equipment.
(v) For each voltage, the maximum pre-dialling delay response shall be measured and recorded on CE-99033, Sheet 10.
4.2.5 Minimum Inter-Digital Pause Response.
(i) Test condition in accordance with Fig. 12 Minimum Inter-Digital Pause Response, except that if this test is done after the successful completion of the impulse response test (refer to 4.2.3) the break pulse setting of the pulse generator is not critical and may be between 40 and 110 milliseconds.
(ii) The circuit condition of the indial circuit shall be equivalent to receiving of loop interrupted dial signalling from the public exchange. Before each test train of pulses the circuit shall be looped to allow for the normal functioning of the exchange line circuit.
(iii) The received dial pulse store shall have a visual display connected to the output marking circuits so as to accurately display the stored count of pulses and trains.
(iv) The pulse generator make pulse shall be increased in 5 millisecond steps, as measured on a timer, and applied to the indial circuit. The lowest value of make pulse that results in the break pulses being identified as single digits in successive trains is the minimum inter-Digital pause response.
(v) Separate tests shall be conducted with the maximum, 50 and the minimum volts applied to the PABX equipment.
(vi) For each voltage, the minimum inter-Digital pause response shall be recorded on CE-99033, Sheet 10 .
4.2.6 Clear Forward Response.
(i) Test condition in accordance with Fig. 12 Clear Forward Response.
(ii) The circuit condition of the indial circuit shall be equivalent to receiving seizure signal from the public exchange. The seizure signal shall be interrupted periodically by the application of clear forward signals from the public exchange. A pulse generator shall be used to similate these conditions.
(iii) The accuracy of readings shall be $\pm 0.5$ milliseconds in 1 millisecond step.
(iv) The smallest duration of clear forward signal that will enable the indial circuit to return release guard and blocking signal shall be measured and recorded on CE-99033 Sheet 10.
4.2.7 Reversal Blink Detection and Timing on Answer.
(i) Test condition in accordance with Fig. 12 Reversal Blink Detection and Timing.
(ii) Circuit condition of the indial circuit shall be equivalent to an indialled call;
(a) being answered by the called extension,
(b) unanswered and automatically signalled at the operator's console, being
answered by the operator. answered by the operator.
(iii) The accuracy of the readings shall be $\pm 0.5$ milliseconds in 1 millisecond steps.
(iv) The performance of the polarity on answer condition maintained through all enquiry and transfer functions of the PABX equipment and the presence of blink and its duration are to be recorded on CE-99033, Sheet 10.
4.2.8 Clear Back Delay.
(i) Test condition in accordance with Fig. 13 Timing Response.
(ii) The circuit condition of the indial circuit shall be equivalent to a disconnection by the extension (extension clearing signal), or the operator to whom the call has been switched. The time taken from the initial disconnection in each case until the application of clear back signal by the PABX is the clear back delay.
(iii) The accuracy of the readings shall be $\pm 5$ milliseconds in 10 millisecond steps for extension clearing, and $\pm 0.5$ milliseconds in 1 milliescond steps for operator clearing.
(iv) For each type of disconnection the clear back delay shall be measured and recorded on CE-99033 Sheet 11.
4.2.9 Release Guard and Blocking Delay.
(i) Test condition in accordance with Fig. 13 Timing Response.
(ii) The circuit condition of the indial circuit shall be equivalent to the application of clear back signal from the PABX. The time that elapses between the initial application of clear back signal until the application of release guard any blocking signal by the PABX is the release guard and blocking delay.
(iii) The accuracy of the readings shall be $\pm 0.5$ milliseconds in 1 millisecond steps.
(iv) Release guard and blocking delay shall be measured and results recorded on CE-99033 Sheet 11.
4.3 Guard Period and Release Delay After Clearing.
(i) Test condition in accordance with Fig. 13 for all outgoing exchange line circuits.
(ii) Circuit condition - the application of clearing signal by the extension or operator to a seized exchange line. The time that elapses between the application of clearing signal and the release of the guard from the outgoing exchange line circuit is the guard period. The time that elapses between the application of clearing signal and commencement of the PABX switching path release is the release delay.
(iii) The accuracy of readings shall be $\pm 5$ milliseconds in 10 millisecond steps.
(iv) The guard period and release delay shall be measured and results recorded on CE-99033 Sheet 11.

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Drop Back During Interdigital Pause.
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After an extension or operator dials a digit to the public exchange, there may also be sent a short transient pulse, occuring because of inductive components in the exchange line and PABX exchange line circuit. It is preferable that the exchange line circuit is designed so that no transient is produced, although a small duration transient may be acceptable.
(i) Test condition in accordance with Fig. 14 All outgoing and bothway exchange line circuits shall be tested.
(ii) Circuit condition - the interdigital pause following single pulses produced from a dial that has no inherent contact bounce.
(iii) The accuracy of readings shall be $\pm 0.5$ milliseconds in 1 millisecond steps.
(iv) Any transients occuring within the interdigital pause shall be measured and results recorded on CE-99033 Sheet 12.
4.5 AC Clearing Circuit.
4.5.1 Geréral.

The following tests shall apply to all bothway exchange line circuits and they shall be tested under simulated crossbar and step by step public exchange conditions. Details on the AC clearing generator's performance shall be shown on CE-99033 Sheet 13. Relay data for these tests is given in Fig 15 and on CE-99033 Sheet 13.
4.5.2 AC Clearing Sensitivity.
(i) Tests condition in accordance with Fig. 15 Test Clearing Sensitivity.
(ii) Circuit condition - The AC Clearing circuit held in $A C$ clearing conditions, but public exchange in the free unbalanced condition.
(iii) The accuracy of readings shall be
(a) $\pm 0.5$ milliamps in 1 milliamp steps
(b) $\pm 0.5$ volts in 1 volt steps
(iv) Line current and voltage shall be measured, AC clearing sensitivity (Z) determined. All results shall be recorded on CE-99033 Sheet 13.
4.5.3 AC Clearing Unbalance Sensitivity.
(i) Test conditions in accordance with Fig. 15 Test Sensitivity To Line Unbalance.
(ii) Circuit condition - public exchange to extension call with public exchange holding and extension instrument cleared.
(iii) The accuracy of readings shall be $\pm 5$ ohms in 10 ohm steps.
(iv) The results shall be tabulated on CE-99033 Sheet 13.
4.6 Enquiry Circuit.
(i) Test condition in accordance with Fig. 16 Enquiry Circuit. All relevant exchange line circuits shall be tested for
(a) maximum and minimum earth resistances which will initiate enquiry.
(b) minimum duration of the earth pulse which will initiate enquiry.
(ii) Circuit conditions - public exchange to extension, speaking normal.
(iii) The accuracy of readings shall be
(a) $\pm 5$ ohms in 10 steps
(b) $\pm 0.5$ milliseconds in 1 millisecond steps.
(iv) Results shall be recorded on CE-99033 Sheet 14.


|  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |
|  |  |  |  |  |
|  | $\begin{aligned} & \underline{I} \\ & \underline{\theta} \end{aligned}$ | $\begin{aligned} & N \\ & \mathbf{I} \\ & M \\ & \end{aligned}$ | $\begin{array}{\|l\|} N \\ \mathbf{I} \\ 0 \\ \hline \end{array}$ |  |



[^0]


| $\frac{n}{\frac{0}{\partial}} \begin{aligned} & n \\ & 2 \end{aligned}$ |  |  |  |
| :---: | :---: | :---: | :---: |
|  |  |  |  |
|  | $\frac{2}{2}$ $\vdots$ $\vdots$ $\vdots$ $\vdots$ $\vdots$ $\vdots$ | $\begin{aligned} & \text { n } \\ & \stackrel{3}{9} \\ & 0 \\ & 0 \end{aligned}$ | x x 2 2 0 0 0 1 1 |



1. CONNECT TIMER TO READ PERIOD THAT THE LOW RESISTANCE
RING TRIP CONDITION IS MAINTAINED. ACCURACY $\pm 5 \mathrm{~ms}$

## RING TRIP RESITANCE





[^1]

| POWER SUPPLY | RESISTANCE <br> OHMS | CURRENT <br> MA. |
| :---: | :---: | :---: |
| _-VOLTS. (MIN.) |  |  |
| SO VOLTS. |  |  |
| VOLTS (MAX) |  |  |
|  |  |  |



FIG. 11. PABX APPROVAL TESTS
IN-DIAL SIGNALLING PERFORMANCE.


|  | (xvw) $51700^{---}$ |
| :---: | :---: |
|  | SL70^ os |
|  | ('Niw) 'SL7on |
|  aisunai 357nd ヨryw | 人7ddns y3mod |



1. WITH THE BREAK PULSE AT 67 ms , THE MAKE PULSE LENGTH IS INCREASED FROM 200 ms TILL THE IN-DIAL CIRCUIT CORRECTLY
IDENTIFIES THE BREAK PULSES AS SINGLE DIGITS IN SUNTIFIES THE BREAK PULSES AS SINGLEL DIGITS IN RECORDED AS THE
THE ABOVE TABLE. 3SNOdsay ayYMyOd yvan

SET THE PULSE GENERATOR TO SENO SINGLE BREAK PULSES OF 400 ms dURATION ANO THEN SUCCESSIVELY REDUCE THE
BREAK PULSE DURATION. RECORD IN THE TABLE BELOW THE

MIN. DURATION of the break pulse such that the in-dial

|  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
|  |  | $\begin{array}{\|l} n \\ \stackrel{n}{\mathrm{~g}} \\ \stackrel{9}{n} \\ \text { in } \end{array}$ | $\begin{aligned} & \frac{x}{x} \\ & \frac{1}{2} \\ & \stackrel{n}{2} \\ & \stackrel{y}{c} \end{aligned}$ |  |

NAME OF TEST

| POWER SUPPLY | TIME MS |
| :---: | :---: |
| --- VOLTS (MIN.) |  |
| 50 VOLTS |  |
| --- VOLTS (MAX.) |  |
|  |  |



EXCHANGE LINE CIRCUIT CLASSIFICATION P.A.B.X. EQUIPMENT TYPE -

MANUFACTURER -
MANUFACTURERS DRG N2 -
ISSUE NO -
AMBIENT TEMPERATURE DURING TESTS WAS _ _ ${ }^{\circ} \mathrm{C}$.
TEST AT -
BY —

ACCEPTED/REJECTED -
DATE -


FIG. I4. PABX APPROVAL TESTS
DROP BACK DURING INTERDIGITAL PAUSE.


FIG. 15. PABX APPROVAL TESTS


TESTAT -

| Number | Title |
| :--- | :---: |
| 1080 | PABX Design Objectives |
| 1105 | Signalling on Subscribers lines |
| DRAWINGS | Number |
| CE-99033 | Title |
|  | Sheets 1-15 Approval Tests Proforma. |

End of Specification.


[^0]:    METER TO HAVE $\pm 1 \%$ ACCURACY AND FLAT RESPONSE TO $15+60 \mathrm{~Hz}$ AND
    TO MEASURE RMS. VALUE.
    RHEOSTAT TO BE CALIBRATED IN OHMS.
    RHEOSTAT TO BE CALIBRATED IN OHMS.
    REAOINGS TO BE TAKEN AT HIGHEST VALUE OF RI WHICH PERMITS.
    THE RING OETECTION CIRCUIT YD SATISFACTORILY RESPOND.

[^1]:    AN APPRODIATE NUMBER OF TRAINS OF THE SPECIFIED TEST PULSES
    ARE ADQLIED VIA EACH OF THE LINE CONDITIONS TO THE IN-DIAL

