

Australian Post Office

INTERNAL PLANT CORRESPONDENCE COURSE

1975

ARRANGEMENT OF LESSONS FOR 1975 COURSE

Dates to Post Back Assign. Ans.

1	2	3	4	5			
2 JUL	16 JUL	30 JUL	13 AUG	27 AUG		12	SEPT
1,2,3,4M 1,2C 5E	5,6,7M 3,4C 6E	8,11,12M 5C 7,8E	7,8,9C 9,10,11E 13M	14,15M 19,20C 12,13,14E	Revision Period 25 Aug to 12 Sept.	Test	A

Dates to Post Back Assign. Ans.

6	7	8	9	10		
1 OCT	15 OCT	29 OCT	12 NOV	26 NOV		12 DEC
16M 10,11,12C 15S 15,16F 46S	17M 13,14,15, 21C 16S 47S	18M 22C 17,18E 48,49S	19M 23C 19,20,21E	22,23 24E 18,24C 56S	Revision Period 24 Nov to 12 Dec.	Test B



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	Lesson Number	
ASSIGNMENT 1.	1M	Algebra
	2M	Logarithms
	3M	Slide Rule and Transposition of Formulae.
	4 M	Geometry
	10	Ohm's Law
	2C	Equivalent Circuits and Voltage Dividers.
	5E	Diode Characteristics.
ASSIGNMENT 2.	5M	Trigonometry
	6м	Graphs
	7M	Trigonometry (Angles of any Magnitude)
	3C	Kirchoff's Laws.
	4C	Circuit Theorems
	6E	Zener Diode Applications.
ASSIGNMENT 3.	8м	Trigonometry (Compound Angles and Solution of Trigonometrical Equations).
	11M	Exponential Theorem.
	12M	Functionality.
	5C	Comparison Between Circuit Solution Methods.
	7E	Introduction to Transistors.
	8E	Common Emitter Amplifiers.
ASSIGNMENT 4.	7C	τ to π and π to τ Transformation.
	8c	Four-terminal Networks "Z" and "Y" Parameters.
	9C	Four-terminal Networks "H" Parameters.
	9E	Common Emitter Equivalent Circuits.
	10E	Common Emitter Biassing (1).
	11E	Common Emitter Biassing (2).
	13M	Differentiation.

	Lesson Number	
ASSIGNMENT 5	14M	Application of Differentiation.
	15M	Further Differentiation and Applications.
	19C	Filters (1).
	20C	Filters (2).
	12E	Common Emitter Amplifiers - Series Feedback.
	14E	Common Emitter Amplifiers - Shunt Feedback.
	13E	Emitter Follower and Common Base Amplifiers.
Revision,	Tutorials, Exams.	
ASSIGNMENT 6.	16M	Integration.
	1 0C	Phasors (Vectors).
	11C	Complex Numbers (1).
	12C	Complex Numbers (2).
	158	Modulation (1).
	15E	Simple Cascading Techniques.
	16E	Transistor Characteristics and Ratings.
	468	Logic Principles Gates and Boolean Algebra.
ASSIGNMENT 7.	17M	Definite Integrals.
	13C	Using Complex Numbers to Analyse A.C. Circuits.
	14C	Series Resonance.
	15C	Parallel Resonance.
	168	Modulation (2).
	210	Transmission Lines (1).
	47S	Logic Principles : Flip Flops.
ASSIGNMENT 8.	18M	Application of Integration.
	22C	Transmission lines (2).
	17E	Performance Limitations in Common-Emitter Amplifiers.
	18E	Frequency Response of Common-Emitter Amplifiers.
	488	Logic Principles: Logic Families and Number Systems.
	498	Logic Principles - Counters.
ASSIGNMENT 9.	19M	Approximate Integration.
	230	Transmission Lines (3).
	19 E	F.E.T. Characteristics.
	20E	Junction F.E.T. Applications (1).
	21E	Junction F.E.T. Applications (2).
	558	Digital to Analogue Conversion.
	568	Analogue to Digital Conversion.

	Lesson Number	
ASSIGNMENT 10.	22E	I.Cs and Semi-Conductors.
	23E	I.C. Fabrication.
	24E	Operational Amplifiers.
	18C	Attenuators.
	24C	Inductively Coupled Circuits. Introduction to Transformers.

Revision, Tutorials, Exam.